Integral University, Lucknow

Department of Electronics and Communication Engineering

Study and Evaluation Scheme

Program: B.Tech. Electronics and Communication Engineering

Semester V

S. No.	Course code	Course Title	Type of Paper	Period Per hr/week/sem			Evaluation Scheme				Max. Marks	Credit	Total Credits		Attributes						
				L	T	P	CT	TA	Total	ESE				Employability	Entrepreneurship	Skill Development	Gender Equality	Environment & Sustainability	Human Value	Professional Ethics	United Nations Sustainaible Developmer Goals (SDG
	THEORIES																				
1	EC301	Computer Organization &Architecture	Core	3	1	0	40	20	60	40	100	3:1:0	4	√	√	√					4,8
2	EC302	Integrated Circuits	Core	3	1	0	40	20	60	40	100	3:1:0	4	√	√	√					8,9
3	EC303	Communicati on Systems Engineering	Core	3	1	0	40	20	60	40	100	3:1:0	4	√	√	√					4,8
4	EC304	Automatic Control System	Core	3	1	0	40	20	60	40	100	3:1:0	4	√	√	√					4.9
5	EC305	Antenna & Wave Propagation	Core	2	1	0	40	20	60	40	100	2:1:0	3	√	√	√					8,9
6		Departmental Elective-1	Elective	3	1	0	40	20	60	40	100	3:1:0	4								
PRACTICAL																					
6	EC308	Computer Organization & Architecture Lab.	Core	0	0	2	40	20	60	40	100	0:0:2	1	√	√	√					4,11
7	EC309	Integrated Circuits Lab.	Core	0	0	2	40	20	60	40	100	0:0:2	1	√	√	√					4,9
8	EC310	Communication-I Lab.	Core	0	0	2	40	20	60	40	100	0:0:2	1	√	√	√					8,9
9	EC311	System Simulation Lab.	Core	0	0	2	40	20	60	40	100	0:0:2	1	√	√	√					4.9
			Total	**17**	**6**	**8**	**400**	**200**	**600**	**400**	**1000**	**31**	**27**								