Integral University, Lucknow

Department of Electronics and Communication Engineering

Study and Evaluation Scheme

Program: M.Tech Electronic Circuits and Systems Engineering (Full Time)

Semester II

S. No.	Course code	Course Title	Type of Paper	Period Per hr/week/sem			Evaluation Scheme				Max. Marks	Credit	Total Credits		Attributes						
				L	T	P	CT	TA	Total	ESE				Employability	Entrepreneurship	Skill Development	Gender Equality	Environment & Sustainability	Human Value	Professional Ethics	United Nations Sustainaible Developmen Goals (SDG
	THEORIES																				
1	EC506	VLSI Design	Core	3	1	0	40	20	60	40	100	3:1:0	4	√	√	√					4,9
2	EC507	Analog MOS Circuits	Core	3	1	0	40	20	60	40	100	3:1:0	4	√	√	√					8,9
3	EC508	ASIC Design & FPGA	Core	3	1	0	40	20	60	40	100	3:1:0	4	√	√	√					4,9
4	EC509	Fault Modelling & Testingof Electronics Circuits	Core	3	1	0	40	20	60	40	100	3:1:0	4	√	√	√					4,12
PRACTICAL																					
5	EC510	VLSI Design Lab	Core	0	0	2	40	20	60	40	100	0:0:2	2	√	√	√					4,9
			TOTAL	**12**	**4**	**2**	**200**	**100**	**300**	**200**	**500**		**18**								