Integral University, Lucknow

Department of Electronics and Communication Engineering

Study and Evaluation Scheme

Program: M.Tech Electronic Circuits and Systems Engineering (Full Time)

Semester III

S. No.	Course code	Course Title	Type of Paper	Period Per hr/week/sem			Evaluation Scheme				Max. Marks	Credit	Total Credits		Attributes						
				L	T	P	CT	TA	Total	ESE				Employability	Entrepreneurship	Skill Development	Gender Equality	Environment & Sustainability	Human Value	Professional Ethics	United Nations Sustainaible Developmen Goals (SDG
	THEORIES																				
1	EC601	RF Circuit Design & Technology	Core	3	1	0	40	20	60	40	100	3:1:0	4	√	√	√					8,9
2		Departmental Elective-I	Elective	3	1	0	40	20	60	40	100	3:1:0	4								
3		Departmental Elective-II	Elective	3	1	0	40	20	60	40	100	3:1:0	4								
PRACTICAL																					
4	EC608	Seminar	Core	-	-	-	-	-	100	-	100	0:0:0	2	√		√					4
5	EC699	M.Tech. Dissertation	Core	-	-	-	-	-	60	40	100	0:0:0	4	√	√	√					8,9
			TOTAL	**9**	**3**	**0**	**120**	**60**	**340**	**160**	**500**		**18**								